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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

		§		
Applicants:	Rajesh Sundaram et al.	§	Group Art Unit:	2827
		§		
Serial No.:	10/713,841	§	Examiner:	Thong Quoc Le
		§		
Filed:	November 13, 2003	§	Atty. Dkt. No.:	ITL.1062US (P17921)
		§		
		§		
For:	Negatively Biasing Deselected Memory Cells	§ §	Assignee:	Intel Corporation

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION

Sir:

In response to the office action mailed May 1, 2007, please amend the above-referenced patent application as follows: